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; Author : ADI - Apps www.analog.com/MicroConverter

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; Date : April 2002

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; File : ADCcont.asm

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; Hardware : ADuC814

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; Description : Performs ADC conversions in continuous mode at a

; rate of 30.840KSPS (assuming an 2.097152 Mclk).

; Outputs ADC results into a buffer in ram.Continuously

; flashes LED.

; All rate calculations assume an 2.097152MHz Mclk.

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$MOD814 ; Use 8052&ADuC812 predefined symbols

LED EQU P3.3 ; P3.3 drives red LED on eval board

CHAN EQU 0 ; convert this ADC input channel..

; ..chan values can be 0 thru 8

DSEG

ORG 0030H

LENGTH EQU 40

BUFFER: DS LENGTH ; set up buffer in RAM

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; BEGINNING OF CODE

CSEG

ORG 0000h

JMP MAIN ; jump to main program

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; INTERRUPT VECTOR SPACE

ORG 0033H ; (ADC ISR)

CJNE R0,#58H,CONT

JMP EXIT

CONT: MOV @R0,ADCDATAH

INC R0

MOV @R0,ADCDATAL

INC R0

EXIT: RETI

;====================================================================

; MAIN PROGRAM

ORG 004Bh

MAIN:

; PRECONFIGURE...

MOV ADCCON1,#080h ; power up ADC /32 clk cycle + 16 cycles for conversion + 1 acq cycle

MOV ADCCON2,#CHAN ; select channel to convert

; LAUNCH CONTINUOUS CONVERSIONS...

MOV R0,#BUFFER

SETB EA ; enable interrupts

SETB EADC ; enable ADC interrupt

SETB CCONV ; begin continuous conversions

; CONTINUE WITH OTHER CODE...

MOV A,#1 ; delay length

AGAIN: CPL LED ; blink (complement) the LED

CALL DELAY ; delay

JMP AGAIN ; repeat

; the micro is free to continue with other tasks (flashing the LED in

; this case) while the ADC is continuously converting, and results

; are being handled by the ADC interrupt service routine.

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; SUBROUTINE

DELAY: ; Delays by 100ms \* A

; 100mSec based on 2.097152MHZ

; Core Clock

; i.e. default ADuC814 Clock

MOV R1,A ; Acc holds delay variable

DLY0: MOV R2,#022h ; Set up delay loop0

DLY1: MOV R3,#0FFh ; Set up delay loop1

DJNZ R3,$ ; Dec R3 & Jump here until R3 is 0

DJNZ R2,DLY1 ; Dec R2 & Jump DLY1 until R2 is 0

DJNZ R1,DLY0 ; Dec R1 & Jump DLY0 until R1 is 0

RET ; Return from subroutine

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END